

GOVERNMENT COLLEGE OF ENGINEERING AURANGABAD

(An Autonomous Institute of Government of Maharashtra)

Office of the Controller of Examinations

Result of End Semester Examination November/December 2019

Class: S. E. Electronics and Telecommunication Engineering Full Time - Backlog

Seat No.	GE 241 - Engineering Mathematics-III
BE15F04F014	D

Class: B. E. Electronics and Telecommunication Engineering Full Time - Regular Non CBCS

Seat No.	ET441 - Power Electronics	ET442 - Embedded Systems	ET443 - Microwave Engineering	ET444 - Computer Network	ET447 - Electives II - Radar & Satellite Communication	ET448 - Lab: Power Electronics	ET449 - Lab: Embedded Systems	ET450 - Lab: Microwave Engineering	ET451 - Lab: Computer Network	ET454 - Lab: Electives II - Radar & Satellite Communication	ET455 - Project Part I
BE13F04F054	B+	A	B+	A+	A+	A++	A++	A++	A++	A++	A++
BE15F04F058	B	A	A+	A+	A+	A++	A++	A+	A++	A++	A++

Class: B. E. Electronics and Telecommunication Engineering Full Time - Backlog

Seat No.	ET 444 - Computer Network
BE13F04F017	B+